

PORT 1



LF

RF



LF



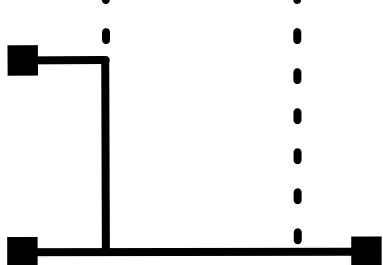
LF



RF



RF



[DUT]

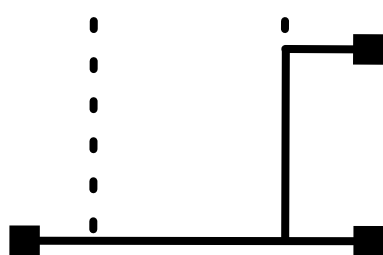


RF

LF



PORT 2



LF



RF



LF



RF